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Microelectronic Circuits, Seventh Edition Sedra/Smith Copyright © 2015 by Oxford University Press Since Then $V_{BE} = V_T \ln \left[\frac{I_B}{I_S} \left(\frac{I_C}{I_B} \right) \right]$ $V_{BE} = 0.69V$ $V_C = -V_{CE} = V_{CC} R_C - I_C R_C$ Since V_C at $+2V$ is higher than V_B at $0.69V$, the collector-to-base pn junction is reverse biased and since the base-to-emitter pn junction is forward biased, the transistor is indeed operating in active mode Where $I_C = \beta I_B = (100)(10E 6) = 1mA$ - Thus, $V_{CE} = 5 - (3k - 0)(1mA) = +2V$ The Base Current $I_B = I_C / \beta = 10 \mu A$...

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Microelectronic Circuits by Adel S. Sedra and Kenneth C. Smith (0195323033) 4.1.1: Current-Voltage Characteristic of the Ideal Diode ideal diode: is most fundamnt nonlinear circuit element two terminal device with circuit symbol to right operates in two modes forward and reverse bias figure 4.1. device symbol with two nodes mode #1: forward bias =

Chapter #3: Diodes
K Single Time Constant (STC) Networks - Low Pass C j R C j KV i 1 1 For the low pass case the output voltage V_o is $(V) (V) (T i O c K) j (T 1$ Where $s=j\omega$ and the cut-off frequency $\omega_c = 1/\tau$ and $\tau = RC$ (the time constant) The normalized magnitude response is $20 \log |c K| j (T 1$ Note ...