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Digital Logic Design - Homework Assignments

9.3 Experiment 2: Digital Logic Gates 446 9.4 Experiment 3: Simplification of Boolean Functions 448 9.5 Experiment 4: Combinational Circuits 450 9.6 Experiment 5: Code Converters 452 9.7 Experiment 6: Design with Multiplexers 453 9.8 Experiment 7: Adders and Subtractors 455 9.9 Experiment 8: Flip-Flops 457 9.10 Experiment 9: Sequential Circuits 460

Mano, Ciletti & Ciletti, Digital Design, 5th Edition | Pearson

Digital Design Homework # 1 (Chapter 1) INSTRUCTIONS: o Show your work (i.e., how you derived your answer or the reason behind your thinking) in addition to your answer. o Homework and solution must be printed and solved on the same paper. o Deadline : Wednesday, 19/9/2012 (not late homework will be accepted)

Digital Design Homework # 1 (Chapter 1)

Each chapter is a combination of theory followed by review exercises to be completed as traditional homework assignments. Full solutions to all of the review exercises are available in the last appendix. Procedures for labs then follow that allow the student to implement the concepts in a hands on manner. The materials required for the labs

Introduction to Digital Logic with Laboratory Exercises

> 63: Digital Design, 4e, by M. Morris Mano, Michael D. Ciletti > 64-Cryptography and Network Security (4th Edition), William Stallings > 65-Communication Networks,2ed, by Alberto Leon-Garcia > 66-Digital Signal Processing,u/e, by Thomas J. Cavicchi > 67- Digital Integrated Circuits-A DESIGN PERSPECTIVE, 2nd,by Jan M. > Rabaey, Anantha

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Chapter 4 - Optimized Implementation of Logic Functions, Sections 4.1 to 4.9 and 4.12 Chapter 5 - Number Representation and Arithmetic Circuits Chapter 6 - Combinational-Circuit Building Blocks Chapter 7 - Flip-Flops, Registers, Counters, and a Simple Processor Chapter 8 - Synchronous Sequential Circuits, Sections: 8.0-8.4 Homework Assignments ...

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Check Homework Tab. • 9/15/15: Quiz 1 will be taking place on 9/22/15 in the class. From Chapter 2 of the Digital Design Book. • 9/24/15: Homework 2 is assigned with DL: 10/01/15 at 5 pm. Check homework tab. • 9/24/15: Quiz 2 will be taking place on 10/01/15 in the class. Scope Chapter 3 of Digital Design Book (from topics covered in the ...

CS Course Webpages

Chapter 2 CPE100: Digital Logic Design I Section 1004: Dr. Morris Combinational Logic Design. Chapter 2 <2> • Introduction • Boolean Equations ... Chapter 2 <9> •Note: New homework instructions starting with HW03 •Homework is due at the beginning of class •Homework must be organized, legible

CPE100: Digital Logic Design I - UNLV Department of ...

The author has a web site with answers to selected homework problems and corrections to errors in the text book at: ... Lecture 9 Chapter 4 examples (pdf) Lecture 10 Sample Exam 1 (pdf) Sample Exam 1 Solution (pdf) Lecture 11: Lecture 12: ... More State Machine Design Examples. Lecture 38: Lecture 39: Lecture 40: Sample Exam 5 (pdf) Sample Exam ...