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## Digital Systems Design Using Verilog

Dr. John has been teaching and conducting research in computer architecture and digital systems design for almost two decades. She has coauthored DIGITAL SYSTEMS DESIGN USING VHDL and DIGITAL SYSTEMS DESIGN USING VERILOG and has edited several successful books on computer performance evaluation and workload characterization. She is an IEEE Fellow.

## Digital Systems Design Using Verilog: Roth, Charles, John

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The basic building block of a digital system in Verilog is the module. Modules support the notion of hierarchical modeling. Common functions can be implemented as a module and used inside various other modules. A module using another module doesn't have to worry about the internals of the other module.

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About This Product. DIGITAL SYSTEMS DESIGN USING VERILOG integrates coverage of logic design principles, Verilog as a hardware design language, and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new hardware configurations. A Verilog equivalent of authors Roth and John's previous successful text using VHDL, this practical book presents Verilog constructs side-by-side with hardware, encouraging students to think in terms of

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Hierarchical Modeling with Verilog A Verilog module includes a module name and an interface in the form of a port list - Must specify direction and bitwidth for each port module input adder(A, B, cout, sum ); [3:0] A, B; adder A B output cout; output [3:0] sum; // HDL modeling of // adder functionality cout sum endmodule Don't forget the semicolon!

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