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1993 Edition by Narain D. Arora (Author)
5.0 out of 5 stars 1 rating

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MOSFET Models for VLSI Circuit Simulation

The other recent classics on this subject are Cheng & Hu's MOSFET Modeling & BSIM3 User's Guide (1999), Arora's MOSFET Models for VLSI Circuit Simulation (1993) and Foty's MOSFET Modeling with SPICE (1996).

MOSFET Models for SPICE Simulation: Including BSIM3v3 and

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Pao-Sah Model. Charge-Sheet Model. Piece-Wise Drain Current Model for Enhancement Devices . First Order Model. Bulk-Charge Model. Square-Root Approximation. Drain Current Equation with Square-Root Approximation. Subthreshold Region Model. Limitations of the Model. Drain Current Model for Depletion Devices. Effective Mobility

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MOSFET Models for VLSI Circuit Simulation: Theory and Practice Dr. Narain Arora (auth.) Metal Oxide Semiconductor (MOS) transistors are the basic building block of MOS integrated circuits (I C). Very Large Scale Integrated (VLSI) circuits using MOS technology have emerged as the dominant technology in the semiconductor industry.

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6.3: Charge-Sheet Model |

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